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2019, Download, Digest, Sarguzasht Digest February 2019, Download, Sehar, Sarguzasht Digest February 2019,. Download Sarguzasht digest February 2019 PDF Book is available on Google Drive with direct link and can be read online for free. Pages: 1, words: 92, total size: 2.00 MB, free PDF download. The present invention relates to semiconductor memory devices, and more particularly to a semiconductor memory device capable of dividing an operation program into different operation programs capable of being programmed, and dividing a bit line sense amplifier circuit between non-selection cells in which the bit line in the idle state is charged in the read operation and selection cells in which the bit line in the idle state is charged in the write operation. In recent years, the write voltage to be applied to non-selection memory cells in a mask ROM has been reduced and the voltage necessary to program non-selection memory cells has been reduced to form a power supply line independently from a power supply line for a bit line sense amplifier (hereinafter abbreviated as a sense amplifier) to cut the power consumption. In the conventional semiconductor memory device, when the bit line is at the potential of a H level, the sense amplifier is activated when the voltage of the bit line is not lower than $V_{cc}/2$ and the memory cell is selected (read or written), whereby the potential of the bit line is lowered to $x/2 \times V_{cc}$ when the memory cell is read. In the case of read, the sense amplifier is not activated when the bit line is at the potential of a H level and the potential of the bit line is raised to $V_{cc}/2$. However, if the potential of the bit line in the idle state is determined by the drop of a current and the bit line is maintained at a potential of $V_{cc}/2$ or less, the power consumption is increased by the operation of the sense amplifier in the idle state. Meanwhile, in the conventional semiconductor memory device, when the bit line is at the potential of a H level, the sense amplifier is activated when the voltage of the bit line is not lower than $V_{cc}/2$ and the memory cell is selected (written), whereby the potential of the bit line is lowered to $x/2 \times V_{cc}$ when the memory cell is written. In the case of write, the sense amplifier is not activated when the bit line is at the potential of a H level and the potential of the bit line is

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